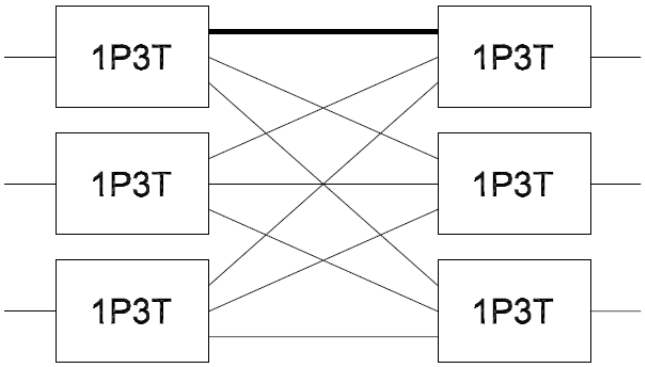
AMD - GROUP

SWITCH MATRIX

Analogue Microwaves Design

Blocking Matrix

The below schematic shows an example of a 3 X 3 non-blocking matrix. It is built with power dividers on the inputs and switches on the outputs. Each input signal is split to all output switches. This confi guration provides greater switching fl exibility because you can have multiple output ports connected to the same input port at the same time

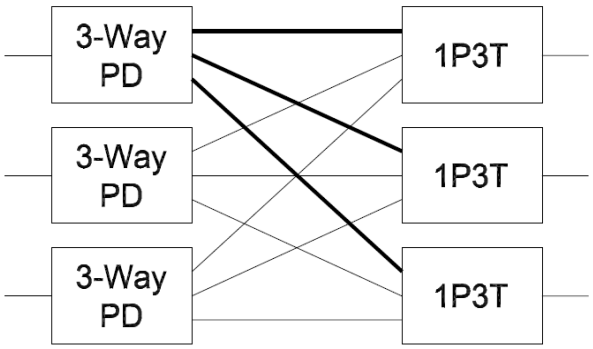


P/N : BMX-IN-OU-F

- IN : Number of imputs
- OU : Number of output
- F : Frequency range

Non-Blocking Matrix

The below schematic shows an example of a 3 X 3 blocking matrix. It is built with switches on both the input ports and output ports. Each output port can only be connected to a single input port. Also, each input port can only be connected to a single output port. This confi guration maximizes the isolation between ports and minimizes the insertion loss of the system



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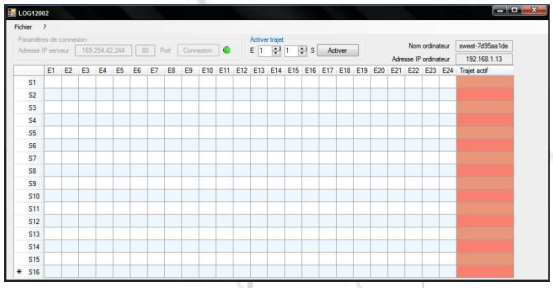

HMI software

Available Options

USB, or Ethernet control by Windows or Labview software	Frequency ranges from DC-18 GHz
Keypad & LCD display for manual control	Various RF connector options (BNC, TNC, SMA, N, etc.)
19”, rack enclosures	50 Ohm and 75 Ohm impedance
Benchtop enclosures	Dual redundant power supplies
Frequency range for blocking matrix : A (1-30MHz), B (30-1000 MHz), C (300MHz-3 GHz), D (400MHz-6GHz), E (6-18 GHz), F(0.5-18GHz), G(18-40GHz)	Frequency range for non blocking matrix : A (DC-30MHz), B (DC-1000 MHz), C (DC-3 GHz), D (DC-6GHz), E (DC-18 GHz), F(DC-18GHz), G(DC-40GHz)

Made In France

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